

REMARKS

Applicant's Statement of Substance of Interview

Applicants wish to express appreciation to Examiner Mehrdad Dastouri for the courtesy of a personal interview which was granted to Applicants' representative Michael Faibisch (Reg. No. 48,427) at the USPTO on April 14, 2005. The Examiner's statement of the substance of the interview is set forth in the Interview Summary, numbered Paper No. 04142005. During the interview, Claim 1 was discussed vis-à-vis the Valesio and Aloni references. In the interview, Applicants' representative pointed out that none of the prior art of record shows or describes analyzing at least some locations of at least some boundaries in an electrical circuit with reference to a corresponding region of acceptable locations to identify defects.

General Remarks

Claims 1 – 18, and 20 – 27 are pending in the application. Claims 1, 12, 20 and 21 are currently amended and claim 19 is canceled without prejudice. Claim 20 includes a self-explanatory amendment shown above to modify dependency and adopt terminology introduced in its parent claim.

Applicant has carefully studied the outstanding Office Action in the present application. The present amendment is intended to be fully responsive to all points of rejection raised by the Examiner in the Office Action mailed January 26, 2005, and is believed to place the application in condition for allowance. Favorable reconsideration and allowance of all claims under consideration is respectfully requested.

Prior Art Rejections

Claims 1 – 8, 12 – 17, 19 and 21 – 26 stand rejected under 35 USC §103(a) as being unpatentable over Valesio et al. (FR 2 687 091) in view of Aloni et al. (US 5,619,429). Claims 9, 10, 11, 18, 20 and 27 stand rejected under 35 USC §103(a) as being unpatentable over Valesio et al. (FR 2 687 091) in view of Aloni et al. (US 5,619,429), and further in view of Bachelder (US 5,974,169).

Valesio (FR 2,687,091) describes a process for computer assisted inspection of cutouts made in a tape by a cutout machine. The inspection method includes extracting contours (boundaries) defining the cutouts and making a polygonal approximation of the cutout. Segments of the polygonal approximation are vectorized, and the series of angles between successive segments are compared with corresponding angles in a theoretical cutout to detect defective cutouts. Aloni describes apparatus and a method for inspection of a patterned object (an electrical circuit) by comparison of the object to a reference. The apparatus and method include hardware defect detection and software postprocessing. Bachelder describes machine vision methods for determining characteristics of an object using boundary points and bounding regions.

Independent claim 1 has been amended and now includes, *inter alia*, the following distinguishing recitation:

a defect identifier operative to receive said representation of boundaries of elements and to analyze at least some locations of at least some boundaries in said representation of boundaries of elements with reference to a corresponding region of acceptable locations to identify defects in said electrical circuit.

Independent claim 12 has been amended and now includes, *inter alia*, the following distinguishing recitation:

analyzing at least one of said boundaries in said representation of boundaries of elements with reference to a corresponding region of acceptable locations to identify defects in said electrical circuit.

Independent claim 21 has been amended and now includes, *inter alia*, the following distinguishing recitation:

analyzing at least one of said boundaries in said representation of boundaries of elements with reference to a corresponding

region of acceptable locations to identify defects in said electrical circuit portion.

Support for the amendment of claims 1, 12 and 21 can be found, *inter alia*, in the written specification at paragraphs 652 – 659, and in Figures 30A – 30C of the drawings.

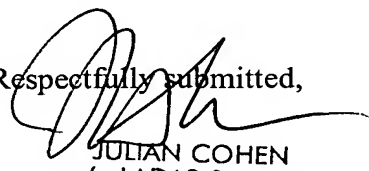
As pointed out by the Applicants' representative with reference to claim 1 in the interview held on April 14, 2005, nothing in any of the prior art of record shows or describes, alone or in combination, analyzing at least some locations of at least some boundaries in an electrical circuit with reference to a corresponding region of acceptable locations to identify defects. The combined teachings of these three documents, even if taken together for what they would have meant as a whole to an artisan of ordinary skill, fail to meet the above-identified requirements of the claims as now amended. In view of the foregoing, Applicants respectfully request that the Examiner withdraw the rejection of independent claims 1, 12 and 21 and all of their dependent claims 2 – 11, 13 - 18, 20 and 21 – 27, under 35 USC §103(a).

Conclusion and Request for Interview

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned attorney at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. _____. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,


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